

Shri Vaishnav Vidyapeeth Vishwavidyalaya													
Choice Based Credit System (CBCS)													
Bachelor of Technology (Electrical and Electronics Engineering) (Session 2018-19)													
IV-SEMESTER													
Sn	Subject Code	Subject Name	Course Code	Teaching			Examination Scheme					Total Marks	Total Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	BTEE401	Electrical Machines-I		3	1	2	60	20	20	30	20	150	5
2	BTEE402	Digital Electronics and Logic Design		3	1	2	60	20	20	30	20	150	5
3	BTEE403	Power System -I		3	1	2	60	20	20	30	20	150	5
4	BTEE404	Electromagnetic Field Theory		3	1	0	60	20	20	0	0	100	4
5	BTEE405	Computer Application for Electrical Engineering		2	0	2	60	20	20	30	20	150	3
6	BTMA301	Applied Mathematics-III		3	1	0	60	20	20	0	0	100	4
Total				17	5	8	360	120	120	120	80	800	26



Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore



Registrar
Shri Vaishnav Vidyapeeth Vishwavidyalaya
INDORE (M.P.)